

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Moore et al.

Serial No.: 09/703,181

Filed: October 30, 2000

Title: Architecture for Efficient
Implementation of Serial Data
Communication Functions On a
Programmable Logic Device (PLD)

Group Art Unit: 2825

Examiner: Liu, A.

Attorney Docket No.: CY-0016

7/c Amdt
9-303
Amdt

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GROUP 2800

RESPONSE TO OFFICE ACTION

5 Mail Stop Non-Fee Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

OFFICIAL

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A. Introductory CommentsThe following is submitted in response to the Office Action dated May 23, 2003, and is
currently due August 25, 2003.

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37 C.F.R. §1.8

I hereby certify that this correspondence is being

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☒ transmitted via facsimile to the United States Patent and Trademark Office to fax number: 1-703-872-9318Date of Transmittal: AUGUST 19, 2003☐ deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents,
P.O. Box 1450, Alexandria, VA 22313-1450.

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Date of Deposit: _____

Typed/Printed Name: Bradley T. Sako

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Signature: [Signature]